



INTEL® FORTRAN COMPILER PLUS INTEL® MATH KERNEL LIBRARY



Intel® Fortran Composer XE 2013

Product Brief

Key Features

- Leadership Application Performance
- Powerful Parallelism Models Simplify Multicore Support
- Industry-leading Intel® MKL math library
- Full source compatibility with Compaq Visual Fortran* compiler

"[Intel Fortran] is stable and offers outstanding performance. We are a small company but big proponents of Intel Fortran!"

Warner Weiss, Manager, Sugars, Int'l LLC

"Intel® Fortran performs beautifully..."

Peter Hahn, Adjunct Assoc. Professor,
University of Pennsylvania

Intel Fortran Composer XE is also included in the following suites:

- Intel® Parallel Studio XE
- Intel® Fortran Studio XE (Linux)
- Intel® Visual Fortran Studio XE (Windows)
- Intel® Cluster Studio XE

OS Support:

- Windows*
- Linux*
- OS X*

Great Application Performance, Serial and Parallel

Intel Visual Fortran Composer XE 2013 SP1 (Windows*) and Intel® Fortran Composer XE 2013 SP1 (Linux*, OS X*) include Intel Fortran and Intel® Math Kernel Library (Intel® MKL) to help you deliver outstanding application performance on systems using Intel® Core™ or Xeon® processors, and compatible processors, and Intel® Xeon Phi™ coprocessors.

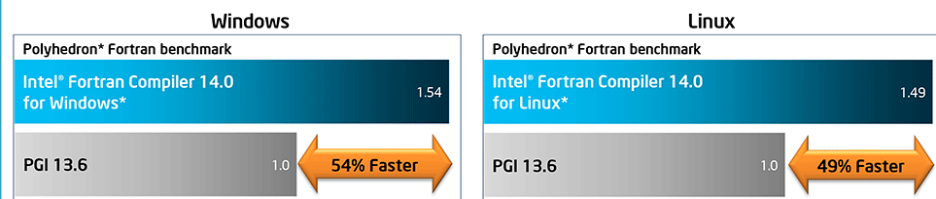
It features compiler-based innovations in vectorization, parallel programming and standards-support to simplify development of performance-oriented Fortran applications. Among these are added support for Fortran 2003, Fortran 2008 and OpenMP* 4.0. And on Windows, it continues to feature full source code compatibility with Compaq Visual Fortran*. For developers on Windows not already using Visual Studio*, Visual Fortran Composer XE includes the Microsoft* Visual Studio* 2010 Shell* so you have a choice of command-line or IDE-based development.

Intel® Math Kernel Library is also included, offering you highly optimized, threaded, specialized functions that speed development and accelerate application performance. Functions in Intel® MKL support Intel® Advanced Vector Extensions (AVX.) This gives you faster floating point operations in mathematical domains such as linear algebra, vector math and vector statistics.

Performance, compatibility and innovative, easy-to-use parallelism capabilities make Intel Fortran Composer XE a powerful and productive tool. Download an eval today and give it a try!

Attention Fortran developers also using C or C++: Intel® Composer XE includes everything above plus the Intel® C++ Compiler, with Intel® Cilk Plus, Intel® Threading Building Blocks, and Intel® Integrated Performance Primitives. It's a great package for developers who need Fortran and C/C++. Take advantage of significant savings compared to purchasing individual components. It's available on Windows and Linux. On Windows, use of this suite requires that you have Microsoft Visual Studio 2008, 2010, or 2012.

Industry Leading Performance using the Intel® Fortran Compiler (Higher is Better)



Configuration Info - Compiler Versions: Intel® Fortran 14.0, PGI 13.6; Hardware: Blue Hills ATX Media I/O Desktop DZ77BH-55K-00; Intel® Core™ i7-3770K CPU @ 3.50GHz; TurboBoost is on; HyperThreading is off; 16GB RAM; Windows Operating System: Windows 7 Enterprise, Service pack 1; Linux Operating System: Red Hat Enterprise Linux Server release 6.3 (Santiago); Kernel 2.6.32-279.el6.x86_64; Compiler Options (Windows and Linux): Intel Fortran compiler 14.0 ifort -O3 -fast -parallel -ipo -no-prec-div -xP -xSSE -xSSE3 -xSSE4.1 -xSSE4.2 -xAVX -xAVX2 -xAVX512 -xAVX512F -xAVX512CD -xAVX512VBQ -xAVX512VBQ_FMA -xAVX512VBQ_FMA3 -xAVX512VBQ_FMA4 -xAVX512VBQ_FMA5 -xAVX512VBQ_FMA6 -xAVX512VBQ_FMA7 -xAVX512VBQ_FMA8 -xAVX512VBQ_FMA9 -xAVX512VBQ_FMA10 -xAVX512VBQ_FMA11 -xAVX512VBQ_FMA12 -xAVX512VBQ_FMA13 -xAVX512VBQ_FMA14 -xAVX512VBQ_FMA15 -xAVX512VBQ_FMA16 -xAVX512VBQ_FMA17 -xAVX512VBQ_FMA18 -xAVX512VBQ_FMA19 -xAVX512VBQ_FMA20 -xAVX512VBQ_FMA21 -xAVX512VBQ_FMA22 -xAVX512VBQ_FMA23 -xAVX512VBQ_FMA24 -xAVX512VBQ_FMA25 -xAVX512VBQ_FMA26 -xAVX512VBQ_FMA27 -xAVX512VBQ_FMA28 -xAVX512VBQ_FMA29 -xAVX512VBQ_FMA30 -xAVX512VBQ_FMA31 -xAVX512VBQ_FMA32 -xAVX512VBQ_FMA33 -xAVX512VBQ_FMA34 -xAVX512VBQ_FMA35 -xAVX512VBQ_FMA36 -xAVX512VBQ_FMA37 -xAVX512VBQ_FMA38 -xAVX512VBQ_FMA39 -xAVX512VBQ_FMA40 -xAVX512VBQ_FMA41 -xAVX512VBQ_FMA42 -xAVX512VBQ_FMA43 -xAVX512VBQ_FMA44 -xAVX512VBQ_FMA45 -xAVX512VBQ_FMA46 -xAVX512VBQ_FMA47 -xAVX512VBQ_FMA48 -xAVX512VBQ_FMA49 -xAVX512VBQ_FMA50 -xAVX512VBQ_FMA51 -xAVX512VBQ_FMA52 -xAVX512VBQ_FMA53 -xAVX512VBQ_FMA54 -xAVX512VBQ_FMA55 -xAVX512VBQ_FMA56 -xAVX512VBQ_FMA57 -xAVX512VBQ_FMA58 -xAVX512VBQ_FMA59 -xAVX512VBQ_FMA60 -xAVX512VBQ_FMA61 -xAVX512VBQ_FMA62 -xAVX512VBQ_FMA63 -xAVX512VBQ_FMA64 -xAVX512VBQ_FMA65 -xAVX512VBQ_FMA66 -xAVX512VBQ_FMA67 -xAVX512VBQ_FMA68 -xAVX512VBQ_FMA69 -xAVX512VBQ_FMA70 -xAVX512VBQ_FMA71 -xAVX512VBQ_FMA72 -xAVX512VBQ_FMA73 -xAVX512VBQ_FMA74 -xAVX512VBQ_FMA75 -xAVX512VBQ_FMA76 -xAVX512VBQ_FMA77 -xAVX512VBQ_FMA78 -xAVX512VBQ_FMA79 -xAVX512VBQ_FMA80 -xAVX512VBQ_FMA81 -xAVX512VBQ_FMA82 -xAVX512VBQ_FMA83 -xAVX512VBQ_FMA84 -xAVX512VBQ_FMA85 -xAVX512VBQ_FMA86 -xAVX512VBQ_FMA87 -xAVX512VBQ_FMA88 -xAVX512VBQ_FMA89 -xAVX512VBQ_FMA90 -xAVX512VBQ_FMA91 -xAVX512VBQ_FMA92 -xAVX512VBQ_FMA93 -xAVX512VBQ_FMA94 -xAVX512VBQ_FMA95 -xAVX512VBQ_FMA96 -xAVX512VBQ_FMA97 -xAVX512VBQ_FMA98 -xAVX512VBQ_FMA99 -xAVX512VBQ_FMA100 -xAVX512VBQ_FMA101 -xAVX512VBQ_FMA102 -xAVX512VBQ_FMA103 -xAVX512VBQ_FMA104 -xAVX512VBQ_FMA105 -xAVX512VBQ_FMA106 -xAVX512VBQ_FMA107 -xAVX512VBQ_FMA108 -xAVX512VBQ_FMA109 -xAVX512VBQ_FMA110 -xAVX512VBQ_FMA111 -xAVX512VBQ_FMA112 -xAVX512VBQ_FMA113 -xAVX512VBQ_FMA114 -xAVX512VBQ_FMA115 -xAVX512VBQ_FMA116 -xAVX512VBQ_FMA117 -xAVX512VBQ_FMA118 -xAVX512VBQ_FMA119 -xAVX512VBQ_FMA120 -xAVX512VBQ_FMA121 -xAVX512VBQ_FMA122 -xAVX512VBQ_FMA123 -xAVX512VBQ_FMA124 -xAVX512VBQ_FMA125 -xAVX512VBQ_FMA126 -xAVX512VBQ_FMA127 -xAVX512VBQ_FMA128 -xAVX512VBQ_FMA129 -xAVX512VBQ_FMA130 -xAVX512VBQ_FMA131 -xAVX512VBQ_FMA132 -xAVX512VBQ_FMA133 -xAVX512VBQ_FMA134 -xAVX512VBQ_FMA135 -xAVX512VBQ_FMA136 -xAVX512VBQ_FMA137 -xAVX512VBQ_FMA138 -xAVX512VBQ_FMA139 -xAVX512VBQ_FMA140 -xAVX512VBQ_FMA141 -xAVX512VBQ_FMA142 -xAVX512VBQ_FMA143 -xAVX512VBQ_FMA144 -xAVX512VBQ_FMA145 -xAVX512VBQ_FMA146 -xAVX512VBQ_FMA147 -xAVX512VBQ_FMA148 -xAVX512VBQ_FMA149 -xAVX512VBQ_FMA150 -xAVX512VBQ_FMA151 -xAVX512VBQ_FMA152 -xAVX512VBQ_FMA153 -xAVX512VBQ_FMA154 -xAVX512VBQ_FMA155 -xAVX512VBQ_FMA156 -xAVX512VBQ_FMA157 -xAVX512VBQ_FMA158 -xAVX512VBQ_FMA159 -xAVX512VBQ_FMA160 -xAVX512VBQ_FMA161 -xAVX512VBQ_FMA162 -xAVX512VBQ_FMA163 -xAVX512VBQ_FMA164 -xAVX512VBQ_FMA165 -xAVX512VBQ_FMA166 -xAVX512VBQ_FMA167 -xAVX512VBQ_FMA168 -xAVX512VBQ_FMA169 -xAVX512VBQ_FMA170 -xAVX512VBQ_FMA171 -xAVX512VBQ_FMA172 -xAVX512VBQ_FMA173 -xAVX512VBQ_FMA174 -xAVX512VBQ_FMA175 -xAVX512VBQ_FMA176 -xAVX512VBQ_FMA177 -xAVX512VBQ_FMA178 -xAVX512VBQ_FMA179 -xAVX512VBQ_FMA180 -xAVX512VBQ_FMA181 -xAVX512VBQ_FMA182 -xAVX512VBQ_FMA183 -xAVX512VBQ_FMA184 -xAVX512VBQ_FMA185 -xAVX512VBQ_FMA186 -xAVX512VBQ_FMA187 -xAVX512VBQ_FMA188 -xAVX512VBQ_FMA189 -xAVX512VBQ_FMA190 -xAVX512VBQ_FMA191 -xAVX512VBQ_FMA192 -xAVX512VBQ_FMA193 -xAVX512VBQ_FMA194 -xAVX512VBQ_FMA195 -xAVX512VBQ_FMA196 -xAVX512VBQ_FMA197 -xAVX512VBQ_FMA198 -xAVX512VBQ_FMA199 -xAVX512VBQ_FMA200 -xAVX512VBQ_FMA201 -xAVX512VBQ_FMA202 -xAVX512VBQ_FMA203 -xAVX512VBQ_FMA204 -xAVX512VBQ_FMA205 -xAVX512VBQ_FMA206 -xAVX512VBQ_FMA207 -xAVX512VBQ_FMA208 -xAVX512VBQ_FMA209 -xAVX512VBQ_FMA210 -xAVX512VBQ_FMA211 -xAVX512VBQ_FMA212 -xAVX512VBQ_FMA213 -xAVX512VBQ_FMA214 -xAVX512VBQ_FMA215 -xAVX512VBQ_FMA216 -xAVX512VBQ_FMA217 -xAVX512VBQ_FMA218 -xAVX512VBQ_FMA219 -xAVX512VBQ_FMA220 -xAVX512VBQ_FMA221 -xAVX512VBQ_FMA222 -xAVX512VBQ_FMA223 -xAVX512VBQ_FMA224 -xAVX512VBQ_FMA225 -xAVX512VBQ_FMA226 -xAVX512VBQ_FMA227 -xAVX512VBQ_FMA228 -xAVX512VBQ_FMA229 -xAVX512VBQ_FMA230 -xAVX512VBQ_FMA231 -xAVX512VBQ_FMA232 -xAVX512VBQ_FMA233 -xAVX512VBQ_FMA234 -xAVX512VBQ_FMA235 -xAVX512VBQ_FMA236 -xAVX512VBQ_FMA237 -xAVX512VBQ_FMA238 -xAVX512VBQ_FMA239 -xAVX512VBQ_FMA240 -xAVX512VBQ_FMA241 -xAVX512VBQ_FMA242 -xAVX512VBQ_FMA243 -xAVX512VBQ_FMA244 -xAVX512VBQ_FMA245 -xAVX512VBQ_FMA246 -xAVX512VBQ_FMA247 -xAVX512VBQ_FMA248 -xAVX512VBQ_FMA249 -xAVX512VBQ_FMA250 -xAVX512VBQ_FMA251 -xAVX512VBQ_FMA252 -xAVX512VBQ_FMA253 -xAVX512VBQ_FMA254 -xAVX512VBQ_FMA255 -xAVX512VBQ_FMA256 -xAVX512VBQ_FMA257 -xAVX512VBQ_FMA258 -xAVX512VBQ_FMA259 -xAVX512VBQ_FMA260 -xAVX512VBQ_FMA261 -xAVX512VBQ_FMA262 -xAVX512VBQ_FMA263 -xAVX512VBQ_FMA264 -xAVX512VBQ_FMA265 -xAVX512VBQ_FMA266 -xAVX512VBQ_FMA267 -xAVX512VBQ_FMA268 -xAVX512VBQ_FMA269 -xAVX512VBQ_FMA270 -xAVX512VBQ_FMA271 -xAVX512VBQ_FMA272 -xAVX512VBQ_FMA273 -xAVX512VBQ_FMA274 -xAVX512VBQ_FMA275 -xAVX512VBQ_FMA276 -xAVX512VBQ_FMA277 -xAVX512VBQ_FMA278 -xAVX512VBQ_FMA279 -xAVX512VBQ_FMA280 -xAVX512VBQ_FMA281 -xAVX512VBQ_FMA282 -xAVX512VBQ_FMA283 -xAVX512VBQ_FMA284 -xAVX512VBQ_FMA285 -xAVX512VBQ_FMA286 -xAVX512VBQ_FMA287 -xAVX512VBQ_FMA288 -xAVX512VBQ_FMA289 -xAVX512VBQ_FMA290 -xAVX512VBQ_FMA291 -xAVX512VBQ_FMA292 -xAVX512VBQ_FMA293 -xAVX512VBQ_FMA294 -xAVX512VBQ_FMA295 -xAVX512VBQ_FMA296 -xAVX512VBQ_FMA297 -xAVX512VBQ_FMA298 -xAVX512VBQ_FMA299 -xAVX512VBQ_FMA300 -xAVX512VBQ_FMA301 -xAVX512VBQ_FMA302 -xAVX512VBQ_FMA303 -xAVX512VBQ_FMA304 -xAVX512VBQ_FMA305 -xAVX512VBQ_FMA306 -xAVX512VBQ_FMA307 -xAVX512VBQ_FMA308 -xAVX512VBQ_FMA309 -xAVX512VBQ_FMA310 -xAVX512VBQ_FMA311 -xAVX512VBQ_FMA312 -xAVX512VBQ_FMA313 -xAVX512VBQ_FMA314 -xAVX512VBQ_FMA315 -xAVX512VBQ_FMA316 -xAVX512VBQ_FMA317 -xAVX512VBQ_FMA318 -xAVX512VBQ_FMA319 -xAVX512VBQ_FMA320 -xAVX512VBQ_FMA321 -xAVX512VBQ_FMA322 -xAVX512VBQ_FMA323 -xAVX512VBQ_FMA324 -xAVX512VBQ_FMA325 -xAVX512VBQ_FMA326 -xAVX512VBQ_FMA327 -xAVX512VBQ_FMA328 -xAVX512VBQ_FMA329 -xAVX512VBQ_FMA330 -xAVX512VBQ_FMA331 -xAVX512VBQ_FMA332 -xAVX512VBQ_FMA333 -xAVX512VBQ_FMA334 -xAVX512VBQ_FMA335 -xAVX512VBQ_FMA336 -xAVX512VBQ_FMA337 -xAVX512VBQ_FMA338 -xAVX512VBQ_FMA339 -xAVX512VBQ_FMA340 -xAVX512VBQ_FMA341 -xAVX512VBQ_FMA342 -xAVX512VBQ_FMA343 -xAVX512VBQ_FMA344 -xAVX512VBQ_FMA345 -xAVX512VBQ_FMA346 -xAVX512VBQ_FMA347 -xAVX512VBQ_FMA348 -xAVX512VBQ_FMA349 -xAVX512VBQ_FMA350 -xAVX512VBQ_FMA351 -xAVX512VBQ_FMA352 -xAVX512VBQ_FMA353 -xAVX512VBQ_FMA354 -xAVX512VBQ_FMA355 -xAVX512VBQ_FMA356 -xAVX512VBQ_FMA357 -xAVX512VBQ_FMA358 -xAVX512VBQ_FMA359 -xAVX512VBQ_FMA360 -xAVX512VBQ_FMA361 -xAVX512VBQ_FMA362 -xAVX512VBQ_FMA363 -xAVX512VBQ_FMA364 -xAVX512VBQ_FMA365 -xAVX512VBQ_FMA366 -xAVX512VBQ_FMA367 -xAVX512VBQ_FMA368 -xAVX512VBQ_FMA369 -xAVX512VBQ_FMA370 -xAVX512VBQ_FMA371 -xAVX512VBQ_FMA372 -xAVX512VBQ_FMA373 -xAVX512VBQ_FMA374 -xAVX512VBQ_FMA375 -xAVX512VBQ_FMA376 -xAVX512VBQ_FMA377 -xAVX512VBQ_FMA378 -xAVX512VBQ_FMA379 -xAVX512VBQ_FMA380 -xAVX512VBQ_FMA381 -xAVX512VBQ_FMA382 -xAVX512VBQ_FMA383 -xAVX512VBQ_FMA384 -xAVX512VBQ_FMA385 -xAVX512VBQ_FMA386 -xAVX512VBQ_FMA387 -xAVX512VBQ_FMA388 -xAVX512VBQ_FMA389 -xAVX512VBQ_FMA390 -xAVX512VBQ_FMA391 -xAVX512VBQ_FMA392 -xAVX512VBQ_FMA393 -xAVX512VBQ_FMA394 -xAVX512VBQ_FMA395 -xAVX512VBQ_FMA396 -xAVX512VBQ_FMA397 -xAVX512VBQ_FMA398 -xAVX512VBQ_FMA399 -xAVX512VBQ_FMA400 -xAVX512VBQ_FMA401 -xAVX512VBQ_FMA402 -xAVX512VBQ_FMA403 -xAVX512VBQ_FMA404 -xAVX512VBQ_FMA405 -xAVX512VBQ_FMA406 -xAVX512VBQ_FMA407 -xAVX512VBQ_FMA408 -xAVX512VBQ_FMA409 -xAVX512VBQ_FMA410 -xAVX512VBQ_FMA411 -xAVX512VBQ_FMA412 -xAVX512VBQ_FMA413 -xAVX512VBQ_FMA414 -xAVX512VBQ_FMA415 -xAVX512VBQ_FMA416 -xAVX512VBQ_FMA417 -xAVX512VBQ_FMA418 -xAVX512VBQ_FMA419 -xAVX512VBQ_FMA420 -xAVX512VBQ_FMA421 -xAVX512VBQ_FMA422 -xAVX512VBQ_FMA423 -xAVX512VBQ_FMA424 -xAVX512VBQ_FMA425 -xAVX512VBQ_FMA426 -xAVX512VBQ_FMA427 -xAVX512VBQ_FMA428 -xAVX512VBQ_FMA429 -xAVX512VBQ_FMA430 -xAVX512VBQ_FMA431 -xAVX512VBQ_FMA432 -xAVX512VBQ_FMA433 -xAVX512VBQ_FMA434 -xAVX512VBQ_FMA435 -xAVX512VBQ_FMA436 -xAVX512VBQ_FMA437 -xAVX512VBQ_FMA438 -xAVX512VBQ_FMA439 -xAVX512VBQ_FMA440 -xAVX512VBQ_FMA441 -xAVX512VBQ_FMA442 -xAVX512VBQ_FMA443 -xAVX512VBQ_FMA444 -xAVX512VBQ_FMA445 -xAVX512VBQ_FMA446 -xAVX512VBQ_FMA447 -xAVX512VBQ_FMA448 -xAVX512VBQ_FMA449 -xAVX512VBQ_FMA450 -xAVX512VBQ_FMA451 -xAVX512VBQ_FMA452 -xAVX512VBQ_FMA453 -xAVX512VBQ_FMA454 -xAVX512VBQ_FMA455 -xAVX512VBQ_FMA456 -xAVX512VBQ_FMA457 -xAVX512VBQ_FMA458 -xAVX512VBQ_FMA459 -xAVX512VBQ_FMA460 -xAVX512VBQ_FMA461 -xAVX512VBQ_FMA462 -xAVX512VBQ_FMA463 -xAVX512VBQ_FMA464 -xAVX512VBQ_FMA465 -xAVX512VBQ_FMA466 -xAVX512VBQ_FMA467 -xAVX512VBQ_FMA468 -xAVX512VBQ_FMA469 -xAVX512VBQ_FMA470 -xAVX512VBQ_FMA471 -xAVX512VBQ_FMA472 -xAVX512VBQ_FMA473 -xAVX512VBQ_FMA474 -xAVX512VBQ_FMA475 -xAVX512VBQ_FMA476 -xAVX512VBQ_FMA477 -xAVX512VBQ_FMA478 -xAVX512VBQ_FMA479 -xAVX512VBQ_FMA480 -xAVX512VBQ_FMA481 -xAVX512VBQ_FMA482 -xAVX512VBQ_FMA483 -xAVX512VBQ_FMA484 -xAVX512VBQ_FMA485 -xAVX512VBQ_FMA486 -xAVX512VBQ_FMA487 -xAVX512VBQ_FMA488 -xAVX512VBQ_FMA489 -xAVX512VBQ_FMA490 -xAVX512VBQ_FMA491 -xAVX512VBQ_FMA492 -xAVX512VBQ_FMA493 -xAVX512VBQ_FMA494 -xAVX512VBQ_FMA495 -xAVX512VBQ_FMA496 -xAVX512VBQ_FMA497 -xAVX512VBQ_FMA498 -xAVX512VBQ_FMA499 -xAVX512VBQ_FMA500 -xAVX512VBQ_FMA501 -xAVX512VBQ_FMA502 -xAVX512VBQ_FMA503 -xAVX512VBQ_FMA504 -xAVX512VBQ_FMA505 -xAVX512VBQ_FMA506 -xAVX512VBQ_FMA507 -xAVX512VBQ_FMA508 -xAVX512VBQ_FMA509 -xAVX512VBQ_FMA510 -xAVX512VBQ_FMA511 -xAVX512VBQ_FMA512 -xAVX512VBQ_FMA513 -xAVX512VBQ_FMA514 -xAVX512VBQ_FMA515 -xAVX512VBQ_FMA516 -xAVX512VBQ_FMA517 -xAVX512VBQ_FMA518 -xAVX512VBQ_FMA519 -xAVX512VBQ_FMA520 -xAVX512VBQ_FMA521 -xAVX512VBQ_FMA522 -xAVX512VBQ_FMA523 -xAVX512VBQ_FMA524 -xAVX512VBQ_FMA525 -xAVX512VBQ_FMA526 -xAVX512VBQ_FMA527 -xAVX512VBQ_FMA528 -xAVX512VBQ_FMA529 -xAVX512VBQ_FMA530 -xAVX512VBQ_FMA531 -xAVX512VBQ_FMA532 -xAVX512VBQ_FMA533 -xAVX512VBQ_FMA534 -xAVX512VBQ_FMA535 -xAVX512VBQ_FMA536 -xAVX512VBQ_FMA537 -xAVX512VBQ_FMA538 -xAVX512VBQ_FMA539 -xAVX512VBQ_FMA540 -xAVX512VBQ_FMA541 -xAVX512VBQ_FMA542 -xAVX512VBQ_FMA543 -xAVX512VBQ_FMA544 -xAVX512VBQ_FMA545 -xAVX512VBQ_FMA546 -xAVX512VBQ_FMA547 -xAVX512VBQ_FMA548 -xAVX512VBQ_FMA549 -xAVX512VBQ_FMA550 -xAVX512VBQ_FMA551 -xAVX512VBQ_FMA552 -xAVX512VBQ_FMA553 -xAVX512VBQ_FMA554 -xAVX512VBQ_FMA555 -xAVX512VBQ_FMA556 -xAVX512VBQ_FMA557 -xAVX512VBQ_FMA558 -xAVX512VBQ_FMA559 -xAVX512VBQ_FMA560 -xAVX512VBQ_FMA561 -xAVX512VBQ_FMA562 -xAVX512VBQ_FMA563 -xAVX512VBQ_FMA564 -xAVX512VBQ_FMA565 -xAVX512VBQ_FMA566 -xAVX512VBQ_FMA567 -xAVX512VBQ_FMA568 -xAVX512VBQ_FMA569 -xAVX512VBQ_FMA570 -xAVX512VBQ_FMA571 -xAVX512VBQ_FMA572 -xAVX512VBQ_FMA573 -xAVX512VBQ_FMA574 -xAVX512VBQ_FMA575 -xAVX512VBQ_FMA576 -xAVX512VBQ_FMA577 -xAVX512VBQ_FMA578 -xAVX512VBQ_FMA579 -xAVX512VBQ_FMA580 -xAVX512VBQ_FMA581 -xAVX512VBQ_FMA582 -xAVX512VBQ_FMA583 -xAVX512VBQ_FMA584 -xAVX512VBQ_FMA585 -xAVX512VBQ_FMA586 -xAVX512VBQ_FMA587 -xAVX512VBQ_FMA588 -xAVX512VBQ_FMA589 -xAVX512VBQ_FMA590 -xAVX512VBQ_FMA591 -xAVX512VBQ_FMA592 -xAVX512VBQ_FMA593 -xAVX512VBQ_FMA594 -xAVX512VBQ_FMA595 -xAVX512VBQ_FMA596 -xAVX512VBQ_FMA597 -xAVX512VBQ_FMA598 -xAVX512VBQ_FMA599 -xAVX512VBQ_FMA600 -xAVX512VBQ_FMA601 -xAVX512VBQ_FMA602 -xAVX512VBQ_FMA603 -xAVX512VBQ_FMA604 -xAVX512VBQ_FMA605 -xAVX512VBQ_FMA606 -xAVX512VBQ_FMA607 -xAVX512VBQ_FMA608 -xAVX512VBQ_FMA609 -xAVX512VBQ_FMA610 -xAVX512VBQ_FMA611 -xAVX512VBQ_FMA612 -xAVX512VBQ_FMA613 -xAVX512VBQ_FMA614 -xAVX512VBQ_FMA615 -xAVX512VBQ_FMA616 -xAVX512VBQ_FMA617 -xAVX512VBQ_FMA618 -xAVX512VBQ_FMA619 -xAVX512VBQ_FMA620 -xAVX512VBQ_FMA621 -xAVX512VBQ_FMA622 -xAVX512VBQ_FMA623 -xAVX512VBQ_FMA624 -xAVX512VBQ_FMA625 -xAVX512VBQ_FMA626 -xAVX512VBQ_FMA627 -xAVX512VBQ_FMA628 -xAVX512VBQ_FMA629 -xAVX512VBQ_FMA630 -xAVX512VBQ_FMA631 -xAVX512VBQ_FMA632 -xAVX512VBQ_FMA633 -xAVX512VBQ_FMA634 -xAVX512VBQ_FMA635 -xAVX512VBQ_FMA636 -xAVX512VBQ_FMA637 -xAVX512VBQ_FMA638 -xAVX512VBQ_FMA639 -xAVX512VBQ_FMA640 -xAVX512VBQ_FMA641 -xAVX512VBQ_FMA642 -xAVX512VBQ_FMA643 -xAVX512VBQ_FMA644 -xAVX512VBQ_FMA645 -xAVX512VBQ_FMA646 -xAVX512VBQ_FMA647 -xAVX512VBQ_FMA648 -xAVX512VBQ_FMA649 -xAVX512VBQ_FMA650 -xAVX512VBQ_FMA651 -xAVX512VBQ_FMA652 -xAVX512VBQ_FMA653 -xAVX512VBQ_FMA654 -xAVX512VBQ_FMA655 -xAVX512VBQ_FMA656 -xAVX512VBQ_FMA657 -xAVX512VBQ_FMA658 -xAVX512VBQ_FMA659 -xAVX512VBQ_FMA660 -xAVX512VBQ_FMA661 -xAVX512VBQ_FMA662 -xAVX512VBQ_FMA663 -xAVX512VBQ_FMA664 -xAVX512VBQ_FMA665 -xAVX512VBQ_FMA666 -xAVX512VBQ_FMA667 -xAVX512VBQ_FMA668 -xAVX512VBQ_FMA669 -xAVX512VBQ_FMA670 -xAVX512VBQ_FMA671 -xAVX512VBQ_FMA672 -xAVX512VBQ_FMA673 -xAVX512VBQ_FMA674 -xAVX512VBQ_FMA675 -xAVX512VBQ_FMA676 -xAVX512VBQ_FMA677 -xAVX512VBQ_FMA678 -xAVX512VBQ_FMA679 -xAVX512VBQ_FMA680 -xAVX512VBQ_FMA681 -xAVX512VBQ_FMA682 -xAVX512VBQ_FMA683 -xAVX512VBQ_FMA684 -xAVX512VBQ_FMA685 -xAVX512VBQ_FMA686 -xAVX512VBQ_FMA687 -xAVX512VBQ_FMA688 -xAVX512VBQ_FMA689 -xAVX512VBQ_FMA690 -xAVX512VBQ_FMA691 -xAVX512VBQ_FMA692 -xAVX512VBQ_FMA693 -xAVX512VBQ_FMA694 -xAVX512VBQ_FMA695 -xAVX512VBQ_FMA696 -xAVX512VBQ_FMA697 -xAVX512VBQ_FMA698 -xAVX512VBQ_FMA699 -xAVX512VBQ_FMA700 -xAVX512VBQ_FMA701 -xAVX512VBQ_FMA702 -xAVX512VBQ_FMA703 -xAVX512VBQ_FMA704 -xAVX512VBQ_FMA705 -xAVX512VBQ_FMA706 -xAVX512VBQ_FMA707 -xAVX512VBQ_FMA708 -xAVX512VBQ_FMA709 -xAVX512VBQ_FMA710 -xAVX512VBQ_FMA711 -xAVX512VBQ_FMA712 -xAVX512VBQ_FMA713 -xAVX512VBQ_FMA714 -xAVX512VBQ_FMA715 -xAVX512VBQ_FMA716 -xAVX512VBQ_FMA717 -xAVX512VBQ_FMA718 -xAVX512VBQ_FMA719 -xAVX512VBQ_FMA720 -xAVX512VBQ_FMA721 -xAVX512VBQ_FMA722 -xAVX512VBQ_FMA723 -xAVX512VBQ_FMA724 -xAVX512VBQ_FMA725 -xAVX512VBQ_FMA726 -xAVX512VBQ_FMA727 -xAVX512VBQ_FMA728 -xAVX512VBQ_FMA729 -xAVX512VBQ_FMA730 -xAVX512VBQ_FMA731 -xAVX512VBQ_FMA732 -xAVX512VBQ_FMA733 -xAVX512VBQ_FMA734 -xAVX512VBQ_FMA735 -xAVX512VBQ_FMA736 -xAVX512VBQ_FMA737 -xAVX512VBQ_FMA738 -xAVX512VBQ_FMA739 -xAVX512VBQ_FMA740 -xAVX512VBQ_FMA741 -xAVX512VBQ_FMA742 -xAVX512VBQ_FMA743 -xAVX512VBQ_FMA744 -xAVX512VBQ_FMA745 -xAVX512VBQ_FMA746 -xAVX512VBQ_FMA747 -xAVX512VBQ_FMA748 -xAVX512VBQ_FMA749 -xAVX512

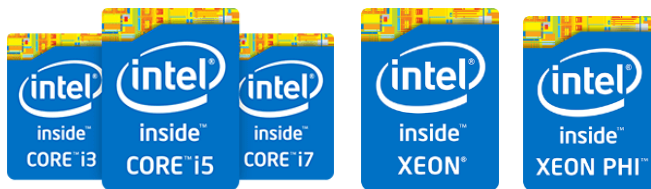
Top Features

Example of Coarray Fortran

```
real, ALLOCATABLE :: grid(:, :)[:, :]
...
ALLOCATE(grid(0:N+1, 0:M+1)[1:P, 1: *]) ! with halo
...
SYNC ALL !...wait for all iterations

grid( 0, 1:M) = grid( N, 1:M)[north_P, me_Q] ! north
grid( N+1, 1:M) = grid( 1, 1:M)[south_P, me_Q] ! south
grid( 1:N, M+1) = grid( 1:N, 1 ) [me_P, east_Q] ! east
grid( 1:N, 0 ) = grid( 1:N, M ) [me_P, west_Q] ! west
```

Intel® Fortran compiler supports Intel® Core® & Xeon® Processors and Intel® Xeon Phi™ Coprocessors



Intel Fortran supports Intel and compatible processors, including application offloading to Intel Xeon Phi coprocessors and the graphics processing units that are part of the Intel Core i3, i5 and i7 processors.

Intel® Fortran – Great Standards Supports

The compiler in Intel Fortran Composer XE 2013 offers great Fortran standards coverage, now with expanded support for Fortran 2003 and 2008 and full source code compatibility with Compaq Visual Fortran. On Windows and Linux, it includes support for coarray Fortran, providing support for multi-cpu shared-memory nodes. Cluster support is available in Intel® Cluster Studio XE product. The benefit is performance for your Fortran applications as they run on individual computers or clusters.

Vectorization, Parallelism Made Powerful

Intel Fortran, like Intel C++, includes support for three industry-leading vectorization and parallelism features associated with Intel Core Processors, Intel Xeon Processors and Intel Xeon Phi coprocessors.

First, the compiler automatically vectorizes and/or automatically parallelizes code for use on systems with Intel® Core™, Intel® Xeon, and compatible processors. These capabilities have been enhanced for broader applicability and improved application performance. And they support the wider vectors in the latest Intel Architecture processors.

Second, when a loop can't be vectorized, GAP – Guided Auto Parallelism can generate a report suggesting source code changes, use of pragmas, or use of compiler options. You remain in control in applying suggestions. In cases where you know your code well, GAP can offer great opportunities for performance improvement.

Third, Intel Fortran supports application development for Intel® Xeon Phi coprocessors using simple directives. Support is extended to Xeon Phi coprocessors hosted on Xeon-based systems using Linux and, new with SP1, Windows. The benefit? Choose your operating environment, maintain your existing programming model, and use of Fortran, to take advantage of outstanding application performance.

Intel® Math Kernel Library Industry-leading high performance math library

Included in Intel® Parallel Studio XE & Intel® Cluster Studio

Linear Algebra	Fast Fourier Transforms	Vector Math	Random Number Generators	Summary Statistics	Data Fitting
<ul style="list-style-type: none">• BLAS• LAPACK• Sparse Solvers	<ul style="list-style-type: none">• Multi-dimensional (up to 7D)• FFTW* Interfaces	<ul style="list-style-type: none">• Trigonometric• Hyperbolic• Exponential, Logarithmic• Power / Root• Rounding	<ul style="list-style-type: none">• Congruent• Recursive• Wichman-Hill• Mersenne Twister• Sobol• Neiderreiter• RDRAND-based	<ul style="list-style-type: none">• Kurtosis• Variation coefficient• Quantiles, order statistics• Min/max• Variance-covariance	<ul style="list-style-type: none">• Splines• Interpolation• Cell search
CLUSTER • ScaLAPACK	CLUSTER • Cluster FFT				

Optimized Math Operations
running on Windows®, Linux® and OS X*

Intel® MKL – Optimization Made Even Easier

One of the easiest ways to take advantage of vectorization and threading is to make calls in your applications to the pre-optimized functions in the Intel® MKL. A wealth of routines is included to optimize math-intensive application performance. Applications using Intel® MKL functions automatically scale on previous, current and future processor architectures. Just re-link to the latest library version and your code is ready to take advantage of the latest processor features available.

Intel® MKL offers the highest-performing implementations of LAPACK and BLAS linear algebra functions and are 100% compatible with these de facto industry standard APIs. Fast Fourier Transforms functions include FFTW*-compatible APIs to ease adoption. Additional routines include vectorized transcendental functions, random number generators for several probability distributions, convolution and correlation routines, and summary statistics functions.

The Rogue Wave® IMSL® Numerical Library for Intel Visual Fortran (Windows only) is also available. This leading library offers the largest collection of commercially-available math and statistical functions for science, technical, and business environments.

Take Comfort – Intel Fortran Composer XE is compatible with the way you work

Intel Visual Fortran Composer XE for Windows integrates into Microsoft Visual Studio* 2008, 2010 and 2012. For those who do not have Visual Studio, it includes the Microsoft Visual Studio 2010 Shell so you have a choice of command-line or IDE-based development. Intel Fortran Composer XE supports the gnu tool chain on leading Linux distributions and Mac OS X. Intel Visual Fortran Composer XE (Windows) and Intel Fortran Composer XE (Linux, OS X) supports all IA-32 and Intel 64 architectures, including the Intel Xeon Phi coprocessor, and includes one year of support. In addition, there's an active community of developers out there sharing their experiences on our Forums.

Take Advantage – Intel Fortran Composer XE supports standards and delivers application performance

Intel Fortran Composer XE 2011 supports performance-oriented industry standards, adding more support for Fortran 2003, Fortran 2008, and OpenMP* 4.0. Coarray support is provided for shared-memory systems and for large-scale compute-clusters with the Intel Fortran included in Intel® Cluster Studio XE 2013. Other Fortran 2008 features include CONTIGUOUS, I/O enhancements, and intrinsic functions, a set of which includes matrix multiply intrinsic functions that support calls into Intel MKL. Fortran 2003 support has also been enhanced, providing complete type-bound procedures such as GENERIC and OPERATOR. Support for Fortran 2003 features such as object-orientation, type-bound procedures and operators, and C++ interoperability continue to make it easier to develop mixed-language applications. Intel Fortran interacts nicely with new C99, C++ 03, and C++11 features that are supported in Intel® C++.

Intel continues to enhance proven compiler features such as the High-Performance Parallel Optimizer (HPO). This powerful capability combines vectorization, parallelization, and loop transformations in a single pass that is faster, more effective, and more reliable than individual, discrete phases. Intel Fortran also automatically vectorizes code for use on systems with conventional Intel® Xeon, Intel® Core, and compatible processors, and includes vectorization tools for applications targeting Intel Xeon Phi coprocessors. When the compiler can't vectorize for use on Intel Xeon and Intel Core processors, you can use Guided Auto-Parallelization (GAP) to get a report suggesting changes so your code will vectorize. Interprocedural optimization and profile-guided optimization continue to provide developers with opportunities to enhance performance by in-lining code and restructuring code based on workload, respectively. Performance is #1 at Intel.

Take it Easy – Intel Math Kernel Library keeps you productive and delivers application performance

With Intel Math Kernel Library, Intel Fortran Composer XE is a lot more than a compiler. It's a library of highly optimized, extensively threaded math routines, including BLAS, LAPACK, ScaLAPACK, sparse solvers, fast Fourier transforms vector math and much more. This library is designed to take full advantage of the wider vector units and growing number of cores on modern processors to deliver maximum scalable performance. Intel Fortran Composer XE ships with lots of sample code and tutorials to simplify development with examples and snippets.

Take a Test Drive – See for yourself how Intel Fortran Composer XE can help deliver application performance

Intel Fortran Composer XE 30-day evaluations are available for download from our web site (<http://intel.ly/sw-tools-eval>). You'll need a system with Visual Studio 2008, 2010 or 2012 for a Windows eval. For Linux, you'll need a system with the gnu tool chain. Check out the link above for more details. The download includes tutorials and lots of code samples, or you can jump right in using your own code. To join the community of your fellow Intel Fortran Composer XE developers, visit the Intel Software Network Forums (<http://software.intel.com/en-us/forums/>) or go to the Intel Fortran Composer XE web site (<http://software.intel.com/en-us/articles/intel-composer-xe/>) and click the support tab.

What's New

Feature	Benefit
Performance Leadership	Provide users of your software a level of performance not provided by other compilers and libraries.
Parallelism Tools and Methods	New with the SP1 release: enhanced support for Fortran 2003, Fortran 2008 and OpenMP* 4.0, and enhanced Linux-based application debugging for Intel Xeon Phi coprocessors. Get better performance faster.
Compatibility	With standards support, development environment compatibility, and compatibility with Compaq Visual Fortran, you can preserve the investment in your code, the knowledge you have in using the development environments, and deliver software with outstanding performance on systems with Intel and compatible processors.

Purchase Options: Language Specific Suites

Several suites are available combining the tools to build, verify and tune your application. The product covered in this product brief is highlighted in blue. Named-user or multi-user licenses along with volume, academic, and student discounts are available.

Suites >>		Intel® Cluster Studio XE	Intel® Parallel Studio XE	Intel® C++ Studio XE	Intel® Fortran Studio XE	Intel® Composer XE	Intel® C++ Composer XE	Intel® Fortran Composer XE
Components	Intel® C / C++ Compiler	•	•	•		•	•	
	Intel® Fortran Compiler	•	•		•	•		•
	Intel® Integrated Performance Primitives ³	•	•	•		•	•	
	Intel® Math Kernel Library ³	•	•	•	•	•	•	•
	Intel® Cilk™ Plus	•	•	•		•	•	
	Intel® Threading Building Blocks	•	•	•		•	•	
	Intel® Inspector XE	•	•	•	•			
	Intel® VTune™ Amplifier XE	•	•	•	•			
	Intel® Advisor XE	•	•	•	•			
	Static Analysis	•	•	•	•			
	Intel® MPI Library	•						
	Intel® Trace Analyzer & Collector	•						
	Rogue Wave IMSL* Library ²							•
Operating System ¹		W, L	W, L	W, L	W, L	W, L	W, L, O	W, L, O

Note: ¹ Operating System: W=Windows*, L= Linux*, O= OS X*. ² Available in Intel® Visual Fortran Composer XE for Windows with IMSL*

³ Not available individually on OS X, it is included in Intel® C++ & Fortran Composer XE suites for OS X

Technical Specifications

Technical Specifications	
Processor Support	Supports both genuine Intel® processors and compatible processors.
Operating Systems	Windows*, Linux* and OS X*
Programming Languages	Fortran
Compatibility	Designed to work with Microsoft development products and leading Linux distributions. It provides expanded 32-bit and 64-bit multicore processor support, including enhanced Intel® AVX support. The Intel Fortran Compiler continues to support established standards, such as Fortran 90, Fortran 77, and Fortran IV, near-complete support for Fortran 2003 and significant parts of Fortran 2008.
System Requirements	Intel Fortran Composer XE is available for IA-32 and Intel® 64 architecture and compatible platforms. For details on hardware and software requirements, please refer to: www.intel.com/software/products/systemrequirements/
Documentation, including Release Notes	[Current link: http://software.intel.com/en-us/articles/intel-fortran-composer-xe-documentation/]



Learn more about Intel Composer XE

- Click or enter the link below:
<http://intel.ly/composer-xe>
- Or scan the QR code on the left



Download a free 30-day evaluation

- Click or enter the link below:
<http://intel.ly/sw-tools-eval>
- Click on 'Compilers and Libraries' link

Optimization Notice

Notice revision #20110804

Intel's compilers may or may not optimize to the same degree for non-Intel microprocessors for optimizations that are not unique to Intel microprocessors. These optimizations include SSE2, SSE3, and SSSE3 instruction sets and other optimizations. Intel does not guarantee the availability, functionality, or effectiveness of any optimization on microprocessors not manufactured by Intel. Microprocessor-dependent optimizations in this product are intended for use with Intel microprocessors. Certain optimizations not specific to Intel microarchitecture are reserved for Intel microprocessors. Please refer to the applicable product User and Reference Guides for more information regarding the specific instruction sets covered by this notice.

